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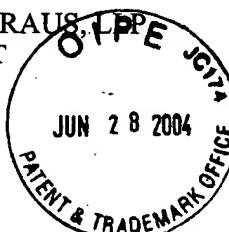
500.39910X00

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/811,387	03/20/2001	Masayuki Miyazaki	500.39910X00	4078

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE IN WHICH OPERATING FREQUENCY, SUPPLY VOLTAGE AND SUBSTRATE BIAS VOLTAGE ARE CONTROLLABLE TO REDUCE POWER CONSUMPTION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/28/2004
EXAMINER	ART UNIT		CLASS-SUBCLASS		
WELLS, KENNETH B	2816		327-534000		

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Renesas Technology Corp.

Tokyo, Japan

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Gregory E. Montone # 28,2004 June 28, 2004

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06/30/2004 EEKUBAY2 00000058 09811387

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